

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
0	-			-				
1	-			-				
2	-			-				
3	-			-				
4	-			-				
5	-			-				
6	-			-				
7	-			-				
8	-			-				
9	-			-				
10	Error Log - Most recent	40010	Register	Read only	Integer			Error Log 1 Module number
11	Error Log - Most recent	40011	Register	Read only	Integer			Error Log 1 Line number
12	Error Log	40012	Register	Read only	Integer			Error Log 2 Module number
13	Error Log	40013	Register	Read only	Integer			Error Log 2 Line number
14	Error Log	40014	Register	Read only	Integer			Error Log 3 Module number
15	Error Log	40015	Register	Read only	Integer			Error Log 3 Line number
16	Error Log	40016	Register	Read only	Integer			Error Log 4 Module number
17	Error Log	40017	Register	Read only	Integer			Error Log 4 Line number
18	Error Log	40018	Register	Read only	Integer			Error Log 5 Module number
19	Error Log	40019	Register	Read only	Integer			Error Log 5 Line number
20	Error Log	40020	Register	Read only	Integer			Error Log 6 Module number
21	Error Log	40021	Register	Read only	Integer			Error Log 6 Line number
22	Error Log	40022	Register	Read only	Integer			Error Log 7 Module number
23	Error Log	40023	Register	Read only	Integer			Error Log 7 Line number
24	Error Log -Oldest	40024	Register	Read only	Integer			Error Log 8 Module number
25	Error Log -Oldest	40025	Register	Read only	Integer			Error Log 8 Line number
26	-			-				
27	Hardware configuration	40027	Register	Read only	Integer			Equivalent to Hardware config in uMatrixWin Utilities
28	-			-				
29	-			-				
30	-			-				
31	-			-				
32	-			-				
33	-			-				
34	-			-				
35	-			-				
36	-			-				
37	-			-				
38	-			-				
39	-			-				
40	-			-				
41	-			-				
42	Modbus address	40042	Register	Read/Write	Integer	247	1	
43	-			-				
44	-			-				
45	-			-				
46	-			-				
47	-			-				
48	-			-				

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
49	-			-				
100	-			-				
101	-			-				
102	-			-				
103	-			-				
104	-			-				
105	-			-				
106	-			-				
107	Temperature	40107	Register	Read only	Float	93.90	-3.50	Divide by 100 in Citect before displaying. Value in Celcius
108	Phase A Frequency	40108	Register	Read only	Float	327.67	7.63	Divide by 100 in Citect before displaying. Value in Hertz
109	-			-				
110	-			-				
111	-			-				
112	Phase A Voltage - Fast	40112	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Value scaled for 110V or 63.5 nominal secondary voltage
113	-			-				
114	-			-				
115	Phase A Voltage - Filtered	40115	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Value scaled for 110V or 63.5 nominal secondary voltage
116	-			-				
117	-			-				
118	-			-				
119	-			-				
120	-			-				
121	-			-				
122	-			-				
123	-			-				
124	-			-				
125	-			-				
126	-			-				
127	-			-				
128	-			-				
129	-			-				
130	-			-				
131	-			-				
132	-			-				
133	-			-				
134	-			-				
135	-			-				
136	-			-				
137	-			-				
138	-			-				
139	-			-				
140	-			-				
141	-			-				
142	-			-				
143	-			-				
144	-			-				
145	-			-				
146	-			-				
147	-			-				

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
148	-			-				
149	-			-				
150	-			-				
151	-			-				
152	-			-				
153	-			-				
154	-			-				
155	-			-				
156	-			-				
157	-			-				
158	-			-				
159	-			-				
160	-			-				
161	-			-				
162	-			-				
163	-			-				
164	-			-				
165	-			-				
166	-			-				
167	-			-				
168	-			-				
169	-			-				
170	Phase A Stage 1 Threshold	40170	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Stage 1 Setpoint
171	Phase A Stage 1 Hysteresis	40171	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Stage 1 Hysteresis
172	Phase A Stage 2 Threshold	40172	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Stage 2 Setpoint
173	Phase A Stage 2 Hysteresis	40173	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Stage 2 Hysteresis
174	Phase A Stage 3 Threshold	40174	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Stage 3 Setpoint
175	Phase A Stage 3 Hysteresis	40175	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Stage 3 Hysteresis
176	Phase A Stage 4 Threshold	40176	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Stage 4 Setpoint
177	Phase A Stage 4 Hysteresis	40177	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Stage 4 Hysteresis
178	-			-				
179	-			-				
180	-			-				
181	-			-				
182	-			-				
183	-			-				
184	-			-				
185	-			-				
186	-			-				
187	-			-				
188	-			-				
189	-			-				
190	-			-				
191	-			-				
192	-			-				
193	-			-				
194	Phase A Undervoltage Threshold	40194	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Undervoltage Setpoint
195	Phase A Undervoltage Hysteresis	40195	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Undervoltage Hysteresis
196	-			-				

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197	-			-				
198	-			-				
199	-			-				
200	-			-				
201	-			-				
202	Stage 1 Delay time	40202	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Stage 1 Delay Time in seconds
203	Stage 1 Reset time	40203	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Stage 1 Reset Time in seconds
204	Stage 2 Delay time	40204	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Stage 2 Delay Time in seconds
205	Stage 2 Reset time	40205	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Stage 2 Reset Time in seconds
206	Stage 3 Delay time	40206	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Stage 3 Delay Time in seconds
207	Stage 3 Reset time	40207	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Stage 3 Reset Time in seconds
208	Stage 4 Delay time	40208	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Stage 4 Delay Time in seconds
209	Stage 4 Reset time	40209	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Stage 4 Reset Time in seconds
210	-			-				
211	-			-				
212	-			-				
213	-			-				
214	-			-				
215	-			-				
216	-			-				
217	-			-				
218	-			-				
219	-			-				
220	-			-				
221	-			-				
222	-			-				
223	-			-				
224	-			-				
225	-			-				
226	-			-				
227	-			-				
228	-			-				
229	-			-				
230	-			-				
231	-			-				
232	-			-				
233	-			-				
234	-			-				
235	-			-				
236	-			-				
237	-			-				
238	-			-				
239	-			-				
240	-			-				
241	-			-				
242	-			-				
243	-			-				
244	-			-				
245	-			-				

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
246	-			-				
247	-			-				
248	-			-				
249	-			-				
250	-			-				
251	-			-				
252	-			-				
253	-			-				
254	-			-				
255	-			-				
256	-			-				
257	-			-				
258	-			-				
259	-			-				
260	-			-				
261	-			-				
262	-			-				
263	-			-				
264	-			-				
265	-			-				
266	-			-				
267	-			-				
268	-			-				
269	-			-				
556	Phase A Calibration Adjust	40556	Register	Read/Write	Float	5.00	-5.00	Divide by 100 in Citect before displaying.
557	-			-				
558	-			-				
559	-			-				
560	-			-				
561	-			-				
562	-			-				
563	-			-				
564	-			-				
565	-			-				
566	-			-				
567	-			-				
568	-			-				
569	-			-				
570	-			-				
571	-			-				
572	-			-				
573	-			-				
574	-			-				
575	-			-				
576	-			-				
577	-			-				
578	-			-				
579	-			-				
600	Remote Enable Input State	00600	Coil	Read only	Bit	TRUE	FALSE	True if remotely enabled.

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
601	-			-				
602	Remote Flags Reset Input State	00602	Coil	Read only	Bit	TRUE	FALSE	True to clear front panel LEDs
603	-			-				
604	-			-				
605	-			-				
606	-			-				
607	-			-				
608	"SET" Key State	00608	Coil	Read only	Bit	TRUE	FALSE	True if "SET" Key pressed
609	"DATA" Key State	00609	Coil	Read only	Bit	TRUE	FALSE	True if "DATA" Key pressed
610	"UP" Key State	00610	Coil	Read only	Bit	TRUE	FALSE	True if "UP" Key pressed
611	"DOWN" Key State	00611	Coil	Read only	Bit	TRUE	FALSE	True if "DOWN" Key pressed
612	"SELECT" Key State	00612	Coil	Read only	Bit	TRUE	FALSE	True if "SELECT" Key pressed
613	-			-				
614	-			-				
615	Host Flags Reset Input	00615	Coil	Read only	Bit	TRUE	FALSE	Rests false. Must be pulsed true for at least 40msec to clear front panel LEDs
616	Phase A Stage 1 Exceeded	00616	Coil	Read only	Bit	TRUE	FALSE	True if Phase A voltage is greater than Stage 1 Setpoint
617	Phase A Stage 2 Exceeded	00617	Coil	Read only	Bit	TRUE	FALSE	True if Phase A voltage is greater than Stage 2 Setpoint
618	Phase A Stage 3 Exceeded	00618	Coil	Read only	Bit	TRUE	FALSE	True if Phase A voltage is less than Stage 3 Setpoint
619	Phase A Stage 4 Exceeded	00619	Coil	Read only	Bit	TRUE	FALSE	True if Phase A voltage is less than Stage 4 Setpoint
620	-			-				
621	-			-				
622	-			-				
623	-			-				
624	-			-				
625	-			-				
626	-			-				
627	-			-				
628	Phase A Undervoltage Exceeded	00628	Coil	Read only	Bit	TRUE	FALSE	True if Phase A voltage is less than Undervoltage Setpoint
629	-			-				
630	-			-				
631	-			-				
648	Stage 1 Timing	00648	Coil	Read only	Bit	TRUE	FALSE	True if Stage 1 Timing
649	Stage 1 Tripped	00649	Coil	Read only	Bit	TRUE	FALSE	True if Stage 1 Delay has expired
650	Stage 2 Timing	00650	Coil	Read only	Bit	TRUE	FALSE	True if Stage 2 Timing
651	Stage 2 Tripped	00651	Coil	Read only	Bit	TRUE	FALSE	True if Stage 2 Delay has expired
652	Stage 3 Timing	00652	Coil	Read only	Bit	TRUE	FALSE	True if Stage 3 Timing
653	Stage 3 Tripped	00653	Coil	Read only	Bit	TRUE	FALSE	True if Stage 3 Delay has expired
654	Stage 4 Timing	00654	Coil	Read only	Bit	TRUE	FALSE	True if Stage 4 Timing
655	Stage 4 Tripped	00655	Coil	Read only	Bit	TRUE	FALSE	True if Stage 4 Delay has expired
656	-			-				
657	-			-				
658	-			-				
659	-			-				
660	-			-				
661	-			-				
662	-			-				
663	-			-				
664	-			-				
665	-			-				

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
666	-			-				
667	-			-				
668	-			-				
669	Not undervolts	00669	Coil	Read only	Bit	TRUE	FALSE	True if all Phase voltages are greater than the Undervoltage Setpoint+Hysteresis
670	Reset Flags	00670	Coil	Read only	Bit	TRUE	FALSE	True if remote or host reset flags requested
671	-			-				
672	-			-				
673	Lockout	00673	Coil	Read only	Bit	TRUE	FALSE	True if remote disabled or any Phase voltage is less than Undervoltage Setpoint
674	-			-				
675	Relay Disable State	00675	Coil	Read only	Bit	TRUE	FALSE	True if relay disabled.
676	-			-				
677	-			-				
678	-			-				
679	-			-				
680	Stage 1 Flag Latch	00680	Coil	Read only	Bit	TRUE	FALSE	True if Stage 1 LED is on
681	Stage 2 Flag Latch	00681	Coil	Read only	Bit	TRUE	FALSE	True if Stage 2 LED is on
682	Stage 3 Flag Latch	00682	Coil	Read only	Bit	TRUE	FALSE	True if Stage 3 LED is on
683	Stage 4 Flag Latch	00683	Coil	Read only	Bit	TRUE	FALSE	True if Stage 4 LED is on
684	-			-				
685	-			-				
686	-			-				
687	-			-				
688	-			-				
689	-			-				
690	-			-				
691	-			-				
692	-			-				
693	-			-				
694	-			-				
695	-			-				
760	Remote Enable Sense	00760	Coil	Read/Write	Bit	TRUE	FALSE	True if Apply Volts Enables the relay, False if Remove Volts Enables the relay
761	-			-				
762	Remote Reset Flags Sense	00762	Coil	Read/Write	Bit	TRUE	FALSE	True if Apply Volts clears LEDs, False if Remove Volts clears LEDs
763	-			-				
764	-			-				
765	-			-				
766	-			-				
767	-			-				
768	-			-				
769	-			-				
770	-			-				
771	-			-				
772	-			-				
773	-			-				
774	-			-				
775	-			-				
776	-			-				
777	-			-				
778	-			-				

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
779	-			-				
780	-			-				
781	-			-				
782	-			-				
783	-			-				
784	-			-				
785	-			-				
786	-			-				
787	-			-				
788	-			-				
789	-			-				
790	-			-				
791	-			-				
808	Phase A Stage 1 Sense	00808	Coil	Read/Write	Bit	TRUE	FALSE	True if Overvoltage stage, False if Undervoltage stage
809	Phase A Stage 2 Sense	00809	Coil	Read/Write	Bit	TRUE	FALSE	True if Overvoltage stage, False if Undervoltage stage
810	Phase A Stage 3 Sense	00810	Coil	Read/Write	Bit	TRUE	FALSE	True if Overvoltage stage, False if Undervoltage stage
811	Phase A Stage 4 Sense	00811	Coil	Read/Write	Bit	TRUE	FALSE	True if Overvoltage stage, False if Undervoltage stage
812	-			-				
813	-			-				
814	-			-				
815	-			-				
816	-			-				
817	-			-				
818	-			-				
819	-			-				
820	-			-				
821	-			-				
822	-			-				
823	-			-				
880	Undervoltage Lockout Mode	00880	Coil	Read/Write	Bit	TRUE	FALSE	True is U/V lockout enabled, False if U/V lockout disabled
881	VT Mode	00881	Coil	Read/Write	Bit	TRUE	FALSE	True is Phase - Neutral (not used if VT Tap = 110V), False is Phase - Phase
882	VT Tap	00882	Coil	Read/Write	Bit	TRUE	FALSE	True is 110V, False is 63.5V.
883	-			-				
884	-			-				
885	-			-				
886	-			-				
887	-			-				
888	-			-				
889	-			-				
890	-			-				
891	-			-				
892	-			-				
893	-			-				
894	-			-				
895	-			-				
896	-			-				
897	-			-				
898	-			-				
899	-			-				

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
900	-			-				
901	-			-				
902	-			-				
903	-			-				
920	Host Parity Enable	00920	Coil	Read/Write	Bit	TRUE	FALSE	True if Parity checking enabled for the programming port
921	Host Parity Odd/Even	00921	Coil	Read/Write	Bit	TRUE	FALSE	True if Parity checking is set to Odd for the programming port
922	Host Baud 0	00922	Coil	Read/Write	Bit	TRUE	FALSE	Programming port baud rate selector
923	Host Baud 1	00923	Coil	Read/Write	Bit	TRUE	FALSE	Programming port baud rate selector
924	Host Baud 2	00924	Coil	Read/Write	Bit	TRUE	FALSE	Programming port baud rate selector
925	Host data bits	00925	Coil	Read/Write	Bit	TRUE	FALSE	True if the programming port uses 7 data bits, else 8 data bits
926	Host stop bits	00926	Coil	Read/Write	Bit	TRUE	FALSE	True if the programming port uses 2 stop bits, else 1 stop bit
927	-			-				
928	Modbus Parity Enable	00928	Coil	Read/Write	Bit	TRUE	FALSE	True if Parity checking enabled for the Modbus port
929	Modbus Parity Odd/Even	00929	Coil	Read/Write	Bit	TRUE	FALSE	True if Parity checking is set to Odd for the Modbus port
930	Modbus Baud 0	00930	Coil	Read/Write	Bit	TRUE	FALSE	Modbus port baud rate selector
931	Modbus Baud 1	00931	Coil	Read/Write	Bit	TRUE	FALSE	Modbus port baud rate selector
932	Modbus Baud 2	00932	Coil	Read/Write	Bit	TRUE	FALSE	Modbus port baud rate selector
933	Modbus data bits	00933	Coil	Read/Write	Bit	TRUE	FALSE	True if the Modbus port uses 7 data bits, else 8 data bits
934	Modbus stop bits	00934	Coil	Read/Write	Bit	TRUE	FALSE	True if the Modbus port uses 2 stop bits, else 1 stop bit
935	-			-				
936	Modbus CDB save	00936	Coil	Read/Write	Bit	TRUE	FALSE	Saves current CDB when pulsed True then False
960	-			-				
961	-			-				
962	-			-				
963	-			-				
964	-			-				
965	-			-				
966	-			-				
967	-			-				
968	-			-				
969	-			-				
970	-			-				
971	-			-				
972	-			-				
973	-			-				
974	-			-				
975	-			-				
976	-			-				
977	-			-				
978	-			-				
979	-			-				
980	-			-				
981	-			-				
982	-			-				
983	-			-				
984	-			-				
985	-			-				
986	-			-				
987	-			-				

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
988	-			-				
989	-			-				
990	-			-				
991	-			-				
992	-			-				
993	-			-				
994	-			-				
995	-			-				
996	-			-				
997	-			-				
998	-			-				
999	-			-				
1000	Relay Serial Number	41000	String	Read only	String	-	-	Occupies 5 integer registers
1005	Relay Hardware Configuration	41005	String	Read only	String	-	-	Occupies 2 integer registers
1007	BIOS Version	41007	String	Read only	String	-	-	Occupies 3 integer registers
1010	Software Model	41010	String	Read only	String	-	-	Occupies 7 integer registers
1017	CDB Name	41017	String	Read only	String	-	-	Occupies 8 integer registers
1025	Software Version	41025	String	Read only	String	-	-	Occupies 3 integer registers